

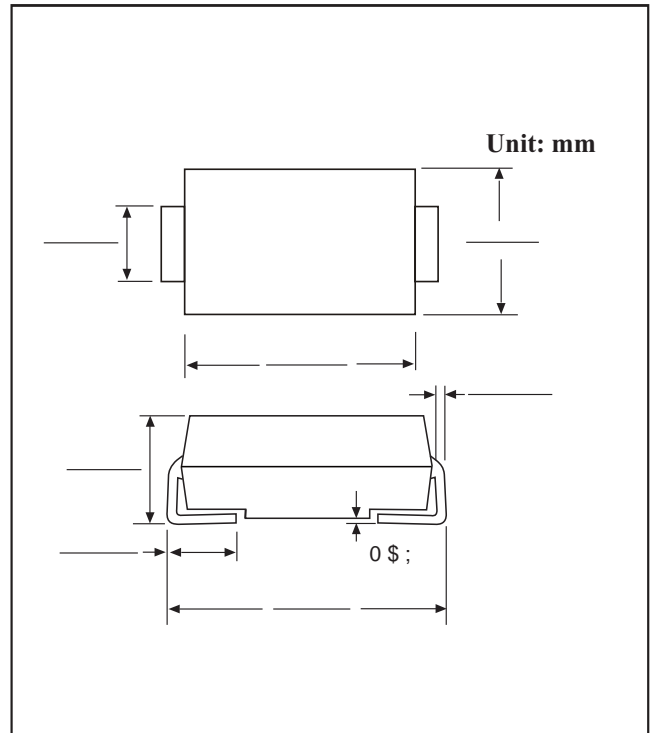
SM % PLASTIC SILICON RECTIFIERS

FEATURES

- "Plastic package has Underwriters Laboratory Flammability Classification 94V-O Utilizing
- "Metal silicon junction ,majority carrier conduction
- "Built-in strain relief
- "For surfacemounted applications
- "Low power loss ,high efficiency,High surge capability
- "High current capability ,Low forward voltage drop
- "For use in low voltage ,high frequency inverters, free wheeling ,and polarity protection applications
- "High temperature soldering guaranteed:260 °C/10 seconds at terminals
- "Component in accordanceto RoHS and WEEE (8

MECHANICAL DATA

- "Case:SM % molded plastic body
- "Terminals:Lead solderable per MIL-STD-750,method 2026
- "Polarity:Color band denotes cathode end



MAXIMUM RATINGS AND CHARACTERISTICS

f & \$ P E L H G W S H U D X Q Q R H W K H U Z L W H G

TYPE NUMBER	SYMBOL	SS 2	S 6	SS 4	SS 5	SS 6	SS 8	SS 1	UNITS
Maximum recurrent peak reverse voltage	V_{RRM}	20	30	40	50	60	80	1 0	V
Maximum RMS voltage	V_{RMS}	14	21	28				7	V
Maximum DC blocking voltage	V_{DC}	20	30	40	50	60	80	1	V
Maximum Average Forward rectified Current0.375"(9.5mm) lead length	$I_{F(AV)}$.0							A
Peak forw ard surge current 8.3ms single half sine-w ave superimposed on rated load	I_{FSM}	0.0							A
Maximum instantaneous forward voltage at .0 A(Note1)	V_F	0.		0.		0.			V
Maximum reverse current at rated DC blocking voltage per diode	@ $T_A=25$								mA
	@ $T_A=100$	0.0			10.0				
7 \ S L F D O 7 K H U P D O 5 H V L V W D Q F H 1 R W H	$R_{\theta JA}$ 5 - /								/W
€ f z r } € t z € t r f r t z € t v 9 , v D	T_I	00							S)
Storage Temperature	T_{STG}	- ---- + 150							
Operation Junction Temperature	T_j	- 5 ---- + 125			- ---- + 150				

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